Length-preserving Bit-stream-based JPEG Encryption

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What?

- Encrypt a given JPEG file
- Don't change its length
- Keep it format-compliant
- What for?
 - Privacy
 - Content control
 - ...





JPEG terminology

- Coefficient: 1 DCT-transformed DC or AC value
- Block: 8x8 DCT coefficients (1 DC, 63 AC)
- iMCU: Group of at least one luma and one chroma block

Encryption approach overview

- Approach performs three operations per iMCU
 - Change order of run-length-value pairs within a block
 - Scramble value bits
 - Swap order of compatible blocks within an iMCU
- Practical considerations
 - Number of blocks per iMCU is small, but constant \rightarrow spatially limited, independently encrypted areas within a picture
 - Self-information increases with number of run-length-value pairs and value bits \to adaptive encryption strength
 - DC coefficients are transmitted separately as differences which are relatively easy to attack \to only encrypt AC coefficients

DC	5	2	0
0	-1	0	
0	0		
1			

5			
0	0	-1	
2			
0	0	0	1

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June 8, 2012 5 / 13







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Approach part 2 – Value bit scrambling

0/3 2/1 0/2 3/1 EOB 100101 111000 0110 1110101 1010 Before 0/3 2/1 0/2 3/1 EOB 100110 111001 0100 1110101 1010 After

June 8, 2012 6 / 13

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Approach part 3 – Intra-iMCU block swapping



- Example: iMCU with 6 blocks (4 luma, 2 chroma)
- Luma and chroma blocks use different Huffman code words
- Block swapping between luma and chroma is not possible
- Inter-luma-block swapping is possible
- Inter-chroma-block swapping is possible (in this example)

- Initialize AES implementation in OFB mode (key-dependent)
- Use *n* "encrypted" bits as *n*-bit PRNG: *arand*(*n*)
- Permutation of n code-word-value-pairs or n blocks: swap each element at index i with element at index arand([log₂(n)])
- Scrambling of *n* value bits: XOR bits with *arand*(*n*)

- Analysis of attack complexity (re-reordering and descrambling)
- Number of run-length-value pairs and value bits influence complexity
- Attack complexity increases with JPEG quality
- \bullet Typical picture: 75% JPEG quality; complexity $\approx 10^{87}$ ways/iMCU
- For comparison: AES-256 key space $pprox 10^{77}$
- Better bruteforce-search AES key than try to decode one iMCU

Security assessment II – Typical attack complexity



- Encrypt DC coefficient differences too
- Swap blocks between different iMCUs (increases attack complexity)
- Encrypt only a part of the picture (Rol)
 - Easy to do on an iMCU basis
 - Hard to signal in a length-preserving way
 - Pseudo solution for signalling: iMCU bitmap in a JPEG comment at the beginning/end of the file (not length-preserving)

- Encryption approach for JPEG files
 - Length-preserving
 - Format-compliant
 - Adaptive
 - Secure
- \bullet Bit-stream-based application \rightarrow no recompression necessary (fast)
- $\bullet\,$ Extensions like RoI encryption possible $\rightarrow\,$ video surveillance

Questions?

June 8, 2012 13 / 13